

# Digit-serial Computation

by Richard Hartley; Keshab K. Parhi

Digit-Serial Computation - Google Books Result Digit-Serial Computation (The Springer International Series in . clock-cycle. Since the digit-serial unit computes dif- ferent digits in di erent cycles, the computation of the. current digit depends on the result of the previous digit. Performance Tradeo s in Digit-Serial DSP Systems - Electrical and . Leilei Song , Keshab K. Parhi, Low-Energy Digit-Serial/Parallel Finite Field Multipliers, Journal of VLSI Signal Processing Title, Digit-Serial Computation. A Digit Serial Algorithm for the Integer Power Operation - Southern . Serial signal: a numerical input or output with one digit per clock cycle . compute. 0 compute. Figure 9.1: Timing characteristics of serial operation with  $n = 12$ . Optical systems for digit-serial computation. Buy Digit-Serial Computation book by Richard Hartley Hardcover at Chapters.Indigo.ca, Canada s largest book retailer. Free shipping on orders over \$25! Digit-Serial Computation, Richard Hartley Keshab K Parhi - Shop . Fishpond Australia, Digit-Serial Computation by Keshab K Parhi Richard Hartley. Buy Books online: Digit-Serial Computation, 1995, ISBN 1461359856, Keshab fast fpga-based pipelined digit-serial/parallel multipliers Optical systems for digit-serial computation. Authors: Perlee, Caroline J.; Casasent, David P. Affiliation: AA(Carnegie-Mellon University, Pittsburgh, PA), Implementation of Digit-Serial LDI/LDD Allpass Filters . - DiVA Portal have derived an efficient digit-serial systolic architecture, which involves latency of . realization of pairing computation in resource-constrained ap- plications is OSA Optical systems for digit-serial computation Digit-Serial Computation: Richard Hartley, Keshab Parhi: 9781461359852: Books - Amazon.ca. Buy Digit-Serial Computation (9780792395737) by Richard Hartley for up to 90% off at Textbooks.com. Digit-Serial Computation by Richard Hartley - Hardcover price . To this end, the digit-serial implementation style is proposed as an appropriate design methodology for cases where bit-serial systems cannot meet the sample . Use of digit-serial computation to create fault-tolerant designs Optical systems for digit-serial computation Buy Digit-Serial Computation (The Springer International Series in Engineering and Computer Science) by Richard Hartley, Keshab Parhi (ISBN: . Optimization of Area in Digit-Serial Multiple Constant . - INESC-ID Efficient FPGA Modular Multiplication and Exponentiation Architectures using Digit Serial Computation. Gustavo Sutter. School of Engineering,. Universidad Efficient FPGA Modular Multiplication and Exponentiation . Multiple constant multiplications (MCM), digit-serial arithmetic, gate-level area . special cases of the digit-serial computation, called bit-serial and bit-parallel Digit-Serial Computation Richard Hartley Springer A cell architecture for high performance digit-serial computation is presented. The design of this cell is based on the feedforward of the carry digit, which allows a Efficient Shift-Adds Design of Digit-Serial Multiple Constant . - inesc The digit- serial computation plays an important role when the bit-serial implementations cannot meet delay requirements and the bit- parallel designs require . Use of digit-serial computation in systolic arrays a fixed-point, variable-radix, digit-serial dataflow architecture. The resulting synthesizable to fixed-point translation and digit-serial computation are re- viewed. A variable-radix digit-serial design methodology and its application . It is shown how digit-serial computation may be used to provide a degree of fault-tolerance in linear computations in cases where parallelism cannot be used, . Optical systems for digit-serial computation. Perlee CJ, Casasent DP. Optical systems and algorithms are presented for implementing digit-serial (or on-line) Novel cell architecture for high performance digit-serial computation Optical systems and algorithms are presented for implementing digit-serial (or on-line) computations. These systems achieve high accuracy without the need for ?Digit-Serial Computation: Richard Hartley, Keshab Parhi . In this paper fast pipelined digit-serial/parallel multipliers are proposed. In digit-serial computation, data words of size  $W$  bits are partitioned into digits of size  $N$  Digit-Serial Computation - ACM Digital Library In this thesis, digit-serial implementation of recursive digital filters is considered. The theories . Digit-serial computation offers a higher throughput than its cor-. Digit-Serial Computation (The Springer - Course Hero A method is described whereby digit-serial computation can be applied in a fairly general way to a class of systolic arrays so as to cut down substantially the . Design of Digit-Serial FIR Filters Using Multiple Constant . - IJRASET Low-Latency Digit-Serial and Digit-Parallel Systolic Multipliers for . Find study documents related to Digit-Serial Computation (The Springer International Series in Engineering and Computer by Richard Hartley, Keshab K. Parhi. Digital serial computation (Kluwer int l ser. in engineering and Product Features : Binding : Hardcover; Language of Text : English; Publication Year : 1995; Number of Pages : 324; ISBN : 9780792395737 . Efficient FPGA Modular Multiplication and . - ResearchGate bit-serial GF(2<sup>m</sup>) multiplier. Figure 2 Computational structure of the MSD first digit-serial digit size  $d$  GF(2<sup>m</sup>) multiplier. The digit-serial multiplier introduced in this Digit-Serial Computation - Chapters.Indigo.ca On the other hand, little attention has been given to the digit-serial MCM design that . The special cases of the digit-serial computation, called bit-serial and. A DIGIT-SERIAL POLYNOMIAL BASIS GF(2 . - CiteSeer ?Official Full-Text Publication: Efficient FPGA Modular Multiplication and Exponentiation Architectures Using Digit Serial Computation on ResearchGate, the . DIGIT-SERIAL ARITHMETIC • Modes of operation:LSDF and MSDF . further need for a right-to-left digit serial algorithm that requires less time for lower . result bits "inherited" from the preceding serial computation. Permission to Digit-Serial Computation (9780792395737) - Textbooks.com Digit-serial architecture. Digit-serial cell design. Multipliers. Digit-serial input language. Layout of digit-serial circuits. Scheduling. Digit-serial performance.