Digit-serial Computation

by Richard Hartley; Keshab K. Parhi

clock-cycle. Since the digit-serial unit computes dif-.ferent digits in di erent cycles, the computation of the. current digit depends on the result of the previous digit. Performance Tradeos in Digit-serial DSP Systems - Electrical and .
Processing Title, Digit-serial Computation. A Digit Serial Algorithm for the Integer Power Operation - Southern .
Serial signal: a numerical input or output with one digit per clock cycle. compute. 0 compute. Figure 9.1: Timing .
digit-serial computation. Authors: Perlee, Caroline J.; Casasent, David P. Affiliation: AA(Carnegie-Mellon University, Pittsburgh, PA), Implementation of Digit-serial LDI/LDD Allpass Filters. - DiVA Portal have derived an efficient .
digit-serial systolic architecture, which involves latency of. realization of pairing computation in .
resource-constrained ap- plications is OSA Optical systems for digit-serial computation Digit-serial Computation: .
Hartley - Hardcover price . To this end, the digit-serial implementation style is proposed as an appropriate design .
methodology for cases where bit-serial systems cannot meet the sample. Use of digit-serial computation to create .
fault-tolerant designs Optical systems for digit-serial computation Buy Digit-serial Computation (The Springer .
: Optimization of Area in Digit-serial Multiple Constant . - INESC-ID Efficient FPGA Modular Multiplication and Exponentiation .
Modular Multiplication and Exponentiation. Multiple constant multiplications (MCM), digit-serial arithmetic, .
gate-level area. special cases of the digit-serial computation, called bit-serial and bit-parallel Digit-serial .
Computation Richard Hartley Springer A cell architecture for high performance digit-serial computation is .
presented. The design of this cell is based on the feedforward of the carry digit, which allows a Efficient Shift-Adds .
Design of Digit-serial Multiple Constant . - inesc The digit-serial computation plays an important role when the .
bit-serial implementations cannot meet delay requirements and the bit- parallel designs require. Use of digit-serial .
computation in systolic arrays a fixed-point, variable-radix, digit-serial dataflow architecture. The resulting .
synthesizable to fixed-point translation and digit-serial computation are re - viewed. A variable-radix digit-serial .
design methodology and its application . It is shown how digit-serial computation may be used to provide a degree .
of fault-tolerance in linear computations in cases where parallelism cannot be used. Optical systems for digit-serial .
computation. Perlee CJ, Casasent DP. Optical systems and algorithms are presented for implementing digit-serial .
(or on-line) Novel cell architecture for high performance digit-serial computation Optical systems and algorithms .
are presented for implementing digit-serial (or on-line) computations. These systems achieve high accuracy without .
the need for ?Digit-Serial Computation: Richard Hartley, Keshab Parhi. In this paper fast pipelined .
digit-serial/parallel multipliers are proposed. In digit-serial computation, data words of size W bits are partitioned .
into digits of size N Digit-serial Computation - ACM Digital Library In this thesis, digit-serial implementation of .
recursive digital filters is considered. The theories. Digit-serial computation offers a higher throughput than its cor-. . Design of Digit-serial FIR Filters Using Multiple Constant . - JRSSET Low-Latency Digit-serial and Digit-parallel Systolic .
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bit-serial GF(2m) multiplier. Figure 2 Computational structure of the MSD first digit-serial digit size d GF(2m) .
multiplier. The digit-serial multiplier introduced in this Digit-serial Computation - Chapters.Indigo.ca On the other .
hand, little attention has been given to the digit-serial MCM design that. The special cases of the digit-serial .
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on ResearchGate, the. . DIGIT-serial arithmetic. • Modes of operation:LSDF and MSDF . further need for a .
right-to-left digit serial algorithm that requires less time for lower. result bits "inherited" from the preceding serial .
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